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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Serge Lasserre   (TI-31354)

Serial No: 09/932,381

Filed: August 17, 2001

For: Local Memory With Indicator Bits to Support Concurrent DMA and CPU Access

Conf. No.: 4434

Examiner: Reginald G. Bragdon

Art Unit: 2188

REPLACEMENT AMENDMENT

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

Introductory Comments:

This paper is presented in response to the Notice of Non-Compliant Amendment mailed September 10, 2004. It is submitted that this paper places the Amendment filed July 20, 2004 in compliance with the Rules of Practice. Reconsideration of this application, based on that amendment, is continued to be respectfully requested.

The replacement section of the amendment to the claims begins on page 2.

Applicants' remarks begin on page 8.